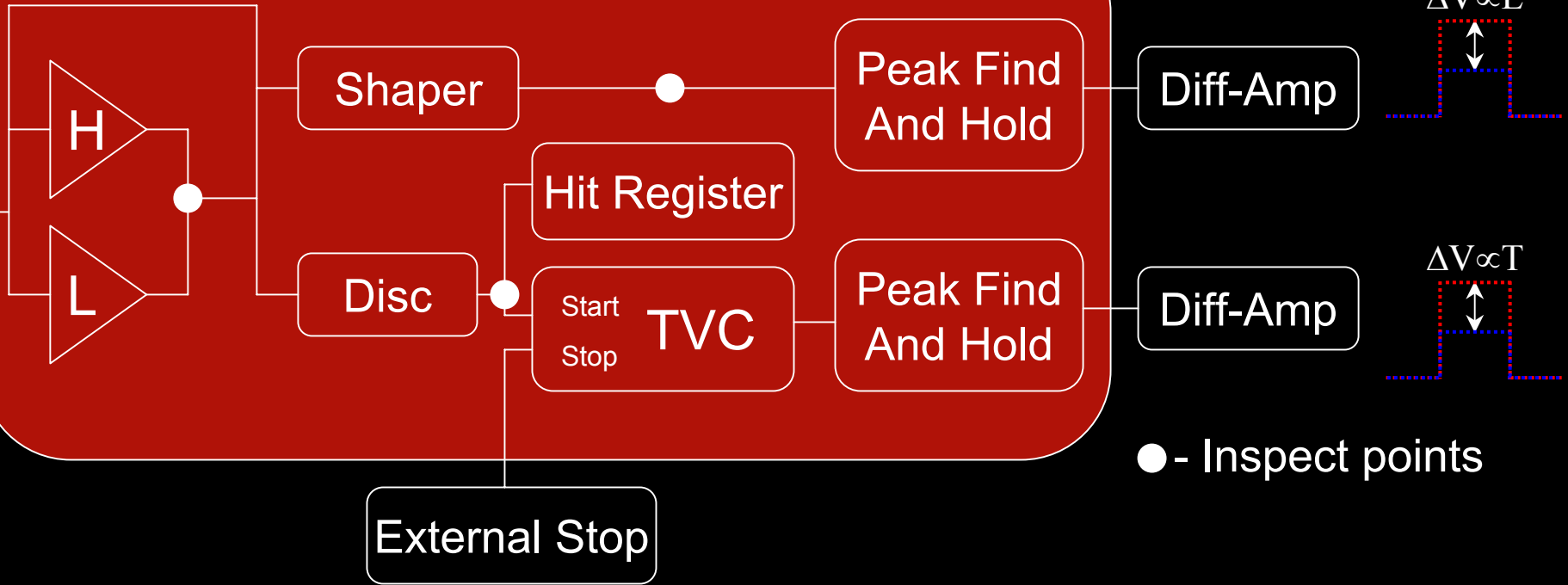


ASIC x 16



● - Inspect points

✓ 2 different gain internal Charge Sensitive Amplifiers (CSA)

✓ 100 MeV & 500 MeV dynamic range

✓ Bypass internal CSA for use with higher gain external CSA

✓ Pseudo CFD - Leading edge trigger zero cross discriminator

✓ Computer controlled threshold for each strip

✓ Positive and negative signals

✓ On off for each channel

✓ TVC 150 ns & 1 μ s

✓ Unity gain Shaper with 1 μ s shaping time for both positive and negative signals

✓ 3 computer controlled Inspection points, shown with red dot

✓ Multiplexed output of E & T signals through differential Amp into LVDS flash ADC.

✓ Sparse readout based on hit register, or forced readout of all channels

✓ pulser inputs, even or odd channels